



PTO/SB/08A (10-01)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet

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of

4

Complete if Known

Application Number	09/589,841
Filing Date	June 8, 2000
First Named Inventor	Farnworth et al.
Group Art Unit	2825
Examiner Name	G. Lee
Attorney Docket Number	3923US (99-0033.00/US)

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
[Handwritten signature]		US-6,251,488	06/26/01	Miller et al.	
		US- 6,259,962	07/10/01	Gothalt	
		US- 6,288,584	07/31/01	Keicher et al.	
		US- 6,326,698	12/04/01	Akram	
		US- 6,391,251	05/21/02	Keicher et al.	
		US- 6,432,752	08/13/02	Farnworth	
		US- 6,482,576	11/19/02	Farnworth et al.	
		US- 6,489,007	12/03/02	Grigg et al.	
		US- 6,514,798	02/04/03	Farnworth	
		US- 6,544,821	04/08/03	Akram	
		US- 6,544,902	04/08/03	Farnworth	
		US- 6,549,821	04/15/03	Farnworth et al.	
		US- 6,562,278	05/13/03	Farnworth et al.	
		US- 6,585,927	07/01/03	Grigg et al.	
		US- 6,593,171	07/15/03	Farnworth	
		US- 6,635,333	10/21/03	Grigg et al.	
		US- [Handwritten symbol]		[Handwritten symbol]	
		US- [Handwritten symbol]		[Handwritten symbol]	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
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² Applicant's unique citation designation number (optional). ³ See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ⁴ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁵ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁶ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁷ Applicant is to place a check mark here if English language Translation is attached.

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First Named Inventor	Farnworth et al.
Group Art Unit	2825
Examiner Name	G. Lee
Attorney Docket Number	39231US (99-0033 00/US)

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		U.S. Patent Application Publication 2002/0066966 A1 to Farnworth, dated June 6, 2002	
		U.S. Patent Application Publication 2002/0098623 A1 to Akram, dated July 25, 2002	
		U.S. Patent Application Publication 2002/0171177 A1 to Kritchman et al., dated November 21, 2002	
		U.S. Patent Application Publication 2002/0182782 A1 to Farnworth, dated December 5, 2002	
		U.S. Patent Application Publication 2003/0003180 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0003380 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0003405 A1 to Farnworth et al., dated January 2, 2003	
		U.S. Patent Application Publication 2003/0043360 A1 to Farnworth, dated March 6, 2003	
		U.S. Patent Application Publication 2003/0068584 A1 to Farnworth et al., dated April 10, 2003	
		U.S. Patent Application Publication 2003/0072926 A1 to Grigg et al., dated April 17, 2003	
		U.S. Patent Application Publication 2003/0077418 A1 to Grigg et al., dated April 24, 2003	

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8/17/04

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Substitute for form 1449A/PTO		Complete if Known	
		Application Number	09/589,841
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	June 8, 2000
		First Named Inventor	Farnworth et al.
		Group Art Unit	2825
		Examiner Name	G. Lee
		Attorney Docket Number	19231JS (99-0033,00/JS)
Sheet	3	of	4

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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		U.S. Patent Application Publication 2003/0089999 A1 to Akram, dated May 15, 2003	
		U.S. Patent Application Publication 2003/0092220 A1 to Akram, dated May 15, 2003	
		U.S. Patent Application Publication 2003/0093173 A1 to Farnworth et al., dated May 15, 2003	
		U.S. Patent Application Publication 2003/0102566 A1 to Farnworth, dated June 5, 2003	
		U.S. Patent Application Publication 2003/0129787 A1 to Farnworth, dated July 10, 2003	
		U.S. Patent Application Publication 2003/0151167 A1 to Kritchman et al., dated August 14, 2003	
		U.S. Patent Application Publication 2003/0203158 A1 to Farnworth et al., dated October 30, 2003	
		MILLER et al., "Maskless Mesoscale Materials Deposition", Deposition Technology, September 2001, pages 20-22	
		MILLER, "New Laser-Directed Deposition Technology", Microelectronic Fabrication, August 2001, page 16	

Examiner Signature		Date Considered	8/17/04
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(use as many sheets as necessary)			
Sheet	A	of	A

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS				
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Q		Webpage, Objet Prototyping the Future, "Objet FullCure700 Series", 1 page		
Q		Wepage, Objet Prototyping the Future, "How it Works", 2 pages		
Q		U.S. Patent Application No. 09/651,930, filed August 31, 2000, entitled "Semiconductor Device Including Leads in Communication with Contact Pads Thereof and a Stereolithographically Fabricated Package Substantially Encapsulating the Leads and Methods for Fabricating the Same", inventor Salman Akram		
Q		U.S. Patent Application No. 10/370,755, filed February 20, 2003, entitled "Chip Scale Package Structures and Method of Forming Conductive Bumps Thereon", inventor Warren M. Farnworth		
Q		U.S. Patent Application No. 10/455,091, filed June 5, 2003, entitled "Methods for Stereolithographic Processing of Components and Assemblies", inventor Warren M. Farnworth		
Q		U.S. Patent Application No. 10/608,749, filed June 26, 2003, entitled "Methods for Labeling Semiconductor Device Components", inventor Grigg et al.		
Q		U.S. Patent Application No. 10/619,918, filed July 15, 2003, entitled "Stereolithographic Methods for Fabricating Hermetic Semiconductor Device Packages and Semiconductor Devices Including Stereolithographically Fabricated Hermetic Packages", inventor Warren M. Farnworth		
Q		U.S. Patent Application No. 10/672,098, filed September 26, 2003, entitled "Apparatus and Methods for Use in Stereolithographic Processing of Components and Assemblies", inventor Warren M. Farnworth		
Q		U.S. Patent Application No. 10/690,417, filed October 20, 2003, entitled "Methods of Coating and Singulating Wafers and Chip-Scale Packages Formed Therefrom", inventor Farnworth et al.		
Q				
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